

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

No amendments have been made to the claims.

1 (Previously presented): A method comprising:  
receiving a wireless wide area network (WWAN) signal;  
filtering, at a WWAN signal handling logic, information included in the WWAN signal;  
determining if an action is to be performed by a processor; and  
when the action is to be performed by the processor, and the processor is in a low power mode, determining from a filter policy if the information warrants the waking of the processor.

2 (Original): The method of claim 1, wherein determining if the processor is to be awakened comprises:  
determining if the action can be delayed; and  
if the action cannot be delayed, awakening the processor.

3 (Original): The method of claim 2, wherein awakening the processor includes transitioning the processor from the low power mode to a normal power mode.

4 (Original): The method of claim 2, further comprising:  
if the action can be delayed, queuing the WWAN signal to enable the processor to perform the action at a subsequent time when the processor is in the normal power mode.

5 (Original): The method of claim 4, wherein the WWAN signal includes short message service (SMS) message, and wherein queuing the WWAN signal includes queuing the SMS message.

6 (Original): The method of claim 1, wherein the WWAN signal is received by a normally- on WWAN module.

7 (Original): The method of claim 6, wherein the normally-on WWAN module receives power from a dedicated battery.

8 (Original): The method of claim 6, wherein the normally-on WWAN module receives power from a power source used by the processor.

9 (Previously presented): A machine-readable medium having stored thereon data representing instructions which, when executed by a wireless wide area network (WWAN) signal handling logic of a module, cause the module to perform operations comprising:

receiving a WWAN signal;  
filtering, at a WWAN signal handling logic, information included in the WWAN signal;  
determining if an action is to be performed by a processor; and  
when the action is to be performed by the processor, and the processor is in a low power mode, determining from a filter policy if the information warrants the waking of the processor.

10 (Original): The machine-readable medium of claim 9, wherein determining if the processor is to be awakened comprises:

determining if the action can be delayed; and  
if the action cannot be delayed, awakening the processor.

11 (Original): The machine-readable medium of claim 10, wherein awakening the processor includes placing the processor in a normal power mode.

12 (Original): The machine-readable medium of claim 10, further comprising:  
if the action can be delayed, queuing the WWAN signal to enable the processor to perform the action at a subsequent time when the processor is in the normal power mode.

13 (Original): The machine-readable medium of claim 12, wherein the WWAN signal includes short message service (SMS) message, and wherein queuing the WWAN signal includes queuing the SMS message.

14. – 22. (Canceled)

23 (Previously presented): An apparatus, comprising:  
an antenna to receive wireless wide area network (WWAN) signals;  
a WWAN signal handling logic at a module coupled to the antenna to filter the WWAN signals; and  
a signal line to send wake up signal to a processor to awaken the processor from a low power mode when the WWAN signal handling logic determines from a filter policy if the information warrants the waking of the processor.

24 (Original): The apparatus of claim 23, further comprising:  
a power source to enable receiving the WWAN signals continuously.

25 (Original): The apparatus of claim 24, wherein the power source is a dedicated power source.

26 (Original): The apparatus of claim 24, wherein the power source is shared with the processor.

27 (Original): The apparatus of claim 23, further comprising:  
a memory to store the WWAN signals when the WWAN signal handling logic determines that the processor is not to be awakened.

28 - 30 (Canceled).